IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Tony G. Hamilton, et al.	§	Art Unit:	2143
		§		
Serial No.:	09/896,563	§	Examiner:	J. Jean-Gilles
		§		
Filed:	June 28, 2001	§	Atty. Dkt. No.:	ITL.1798US
		§		(P11843)
For:	A Method to Provide Direct System Storage Access	§		
	Within a Notebook Computer via a Wireless	§		
	Interconnect and a Low Power High-Speed Data	§		
	Management Bus While the Main CPU Is Idle	§		

Mail Stop **Appeal Brief**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF AMENDED SUMMARY OF CLAIMED SUBJECT MATTER

Dear Sir:

In response to the Notification of Non-Compliant Appeal Brief and pursuant to MPEP § 1205.03(B), attached hereto is an Amended Summary of Claimed Subject Matter.

The independent claims have been mapped to the specification and figures. The Appeal Brief as amended is therefore believed to be in compliance.

No fee is believed to be due with this response. However, the Commissioner is authorized to charge any fee due to Deposit Account No. 20-1504 (ITL1798US).

Respectfully submitted,

Date: 10/17/2007

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V. SUMMARY OF CLAIMED SUBJECT MATTER

17.   A method, comprising:

activating an idle storage device in a computer system to transfer data while a main processor of the computer system remains idle (Specification, paragraph 37; Figure 3, block 370);

executing the data transfer to the storage device (Specification, paragraph 38; Figure 3, block 380); and

returning system resources to an idle state (Specification, paragraph 39; Figure 3, block 390).

27.   An apparatus comprising:

means for activating an idle storage device in a computer system to transfer data while a main processor (Figure 1, item 120) of the computer system remains idle (Specification, paragraphs 22, 37; Figure 1, item 52; Figure 3, block 370);

means for executing the data transfer to the storage device (Specification, paragraph 38; Figure 3, block 380); and

means for returning system resources to an idle state (Specification, paragraphs 21, 39; Figure 1, item 152, Figure 3, block 390).

33.   A machine-readable medium having executable instructions to cause a processor to perform a method, the method comprising:

activating an idle storage device in a computer system to transfer data while a main processor of the computer system remains idle (Specification, paragraph 37;

Figure 3, block 370);

executing the data transfer to the storage device (Specification, paragraph 38; Figure 3, block 380); and

returning system resources to an idle state (Specification, paragraph 39; Figure 3, block 390).

38.   A computer system comprising:

a processor (Figure 1, 120) coupled to a memory (Figure 1) through a bus (Specification, page 5, line 22 to page 6, line 8; Figure 1, 501);

a unit (Figure 1, 152) to activate a storage device in a computer system to transfer data while the processor remains idle (Specification, paragraph 37; Figure 3, block 370), the unit to

execute the data transfer to the storage device (Specification, paragraph 38; Figure 3, block 380), and the unit to

return system resources to an idle state ((Specification, paragraph 39; Figure 3, block 390).